

**Amendments to the Specification**

Please replace the paragraph at page 13, line 30, to page 14, line 14 with the following amended paragraph:

Other integrity checks could involve establishing that various other devices, components or apparatus attached to the platform are present and in correct working order. In one example, the BIOS programs associated with a SCSI controller could be verified to ensure communications with peripheral equipment could be trusted. In another example, the integrity of other devices, for example memory devices or co-processors, on the platform could be verified by enacting fixed challenge/response interactions to ensure consistent results. Where the trusted device 24 is a separable component, some such form of interaction is desirable to provide an appropriate logical binding between the trusted device ~~[[1]]~~<sub>24</sub> and the platform. Also, although in the present embodiment the trusted device 24 ~~utilises~~ utilizes the data bus as its main means of communication with other parts of the platform, it would be feasible, although not so convenient, to provide alternative communications paths, such as hard-wired paths or optical paths. Further, although in the present embodiment the trusted device 24 instructs the main processor 21 to calculate the integrity metric in other embodiments, the trusted device itself is arranged to measure one or more integrity metrics.

Please replace the paragraph at page 15, lines 8-12 with the following amended paragraph:

FIG. 6 illustrates an example of how the system model could be presented to a user on the display 18. The system model 501 has reduced the computer platform to six major components (i.e. input 601, output ~~601~~602, program ~~602~~603, file ~~603~~604, hard drive 605 and trusted device 606) that can be controlled by the trusted platform hardware control 503, as described below.